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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

Application Number	09989762
Filing Date	2001-11-19
First Named Inventor	Kenneth Y. Ogami
Art Unit	2123
Examiner Name	Osborne, Luke R.
Attorney Docket Number	CD01175M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/L.O./	1	5155836	B1	1992-10-13	Jordan et al.	Entire Document
/L.O./	2	5574852	B1	1996-11-12	Bakker, et al.	Entire Document
/L.O./	3	5495593	B1	1996-02-27	Elmer, et al.	Entire Document
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/L.O./	5	5313618	B1	1994-05-17	Pawloski	Entire Document
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/L.O./	9	5329471	B1	1994-07-12	Swoboda, et al.	Entire Document
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/L.O./	1	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>
/L.O./	2	368398	EP	A1	1990-05-01	Thommen	1 page Entire Document	<input type="checkbox"/>
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NON-PATENT LITERATURE DOCUMENTS

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/L.O./	1	Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller;" Kutz ; 38 pages Kutz et al.	<input type="checkbox"/>
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/L.O./	4	Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-fly;" Sullam; 24 pages	<input type="checkbox"/>
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/L.O./	6	Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot;" Nemecek ; 44 pages Nemecek et al.	<input type="checkbox"/>
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/L.O./	8	Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System;" Nemecek; 43 pages	<input type="checkbox"/>

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/L.O./	9	Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer;" Nemecek ; 46 pages Nemecek et al.	<input type="checkbox"/>
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/L.O./	20	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
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/L.O./	23	USPTO Advisory Action for 09/998,848 (CD01177) dated 09/07/05; 3 pages	<input type="checkbox"/>
/L.O./	24	Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" Mar , 28 pages Mar et al.	<input type="checkbox"/>
/L.O./	25	Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks;" Snyder; 28 pages	<input type="checkbox"/>
/L.O./	26	Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode;" Snyder; 31 pages	<input type="checkbox"/>
/L.O./	27	Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface;" Snyder; 32 pages	<input type="checkbox"/>
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/L.O./	31	Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Snyder; 36 pages	<input type="checkbox"/>
	32	Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz; 1 page	<input type="checkbox"/>

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